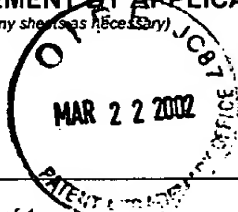


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Complete if Known

Application Number	08/902133
Filing Date	July 29, 1997
First Named Inventor	Forbes, Leonard
Group Art Unit	2815
Examiner Name	Eckert II, George

Sheet 1 of 1

Attorney Docket No: 00303.356US1

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**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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JK		WOLF, "Semiconductor Memory Process Integration", Silicon Processing for the VLSI Era Vol. 2: Process Integration, Lattice Press, California,(1990), pp.623-626	

EXAMINER

DATE CONSIDERED

6/19/02

Substitute Disclosure Statement Form (PTO-1449)

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